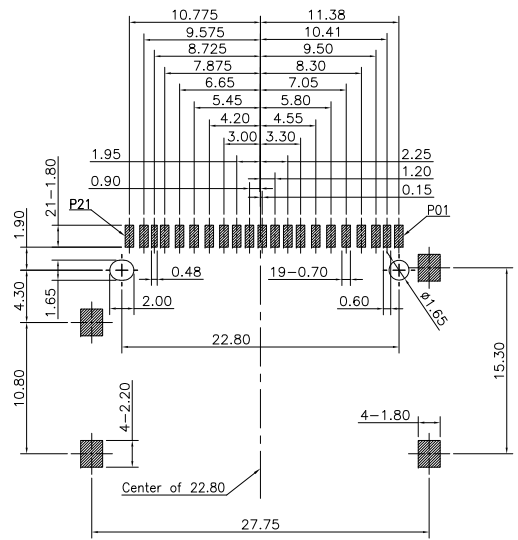
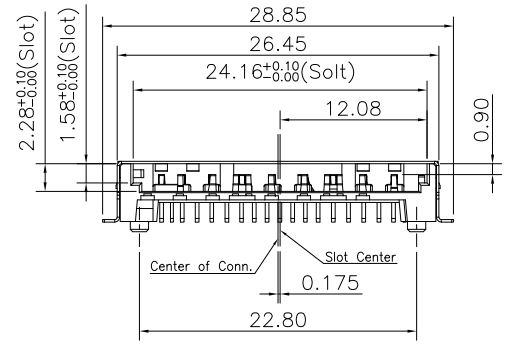
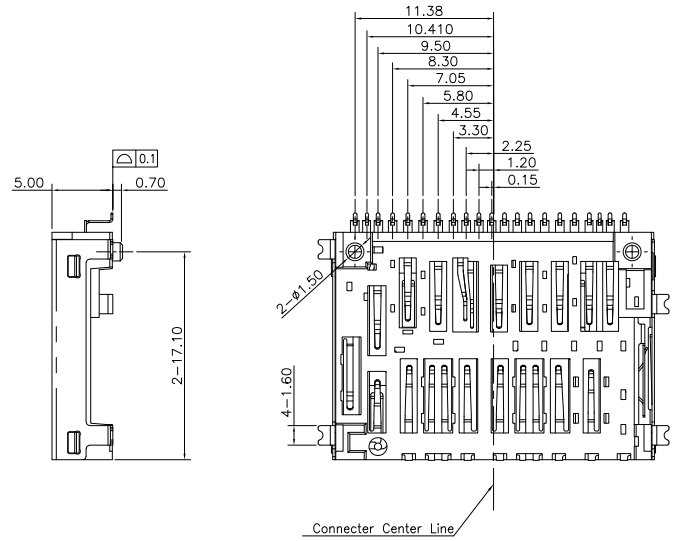
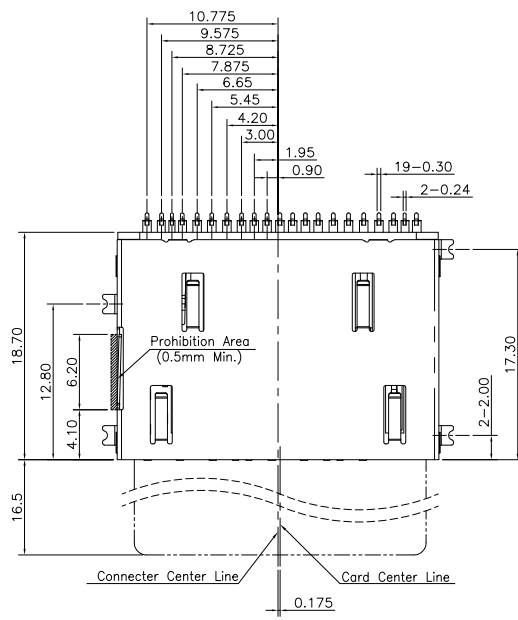


REVISION HISTORY			
REV	DESCRIPTION	Drawing	DATE
01	PROPOSAL	Allen	2022/02/09



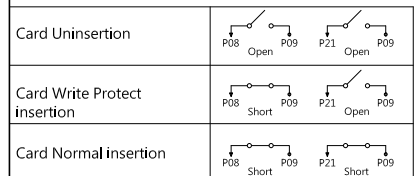
Recommended PCB Layout  
Top View (Tolerance ±0.05)

Note :

1. Material :
  - 1-1. Plastic : LCP, Black, UL94V-0
  - 1-2. Contact : Coper Alloy
  - 1-3. Shell : Stainless Steel
2. Plating :
  - 2-1. Contact :
    - Contact Area: Gold (See Table) Plating Over Ni 50μ" min.
    - Soldertail: Matte Tin 100μ" min. Plating Over Ni 50μ" min.
  - 2-2. Shell :
    - Solderability Ni 50μ" min. Over All.

Pin Assignment Table

Pin No.	SD Mode		UHS-II Mode		SD Express Mode	
	Name	Discription	Name	Discription	Name	Discription
P01			VSS3	Supply Voltage Ground	VSS3	Supply Voltage Ground
P02				Reserved		Reserved area for future pad extension
P03	DATA2	Data Line [Bit 2]			CLKREQ#	Reference clock request signal also used by U1, P04 submodules
P04			DO+	Differential Data 0+	PCIE TX+	
P05	CD/DAT3	Card Detect / Data Line [Bit 3]			PERST#	PE-Reset is a functional reset
P06			DO-	Differential Data 0-	PCIE TX-	
P07	CMD	Command / Response				
P08	CD	Card Detect	CD	Card Detect	CD	Card Detect
P09	VSS1	Supply Voltage Ground	VSS1	Supply Voltage Ground	VSS1	Supply Voltage Ground
P10			VSS4	Supply Voltage Ground	VSS4	Supply Voltage Ground
P11			VDD2	Supply Voltage (1.8V)	VDD2	Supply Voltage (1.8V)
P12	VDD	Supply Voltage	VDD1	Supply Voltage (3.3V)	VDD1	Supply Voltage (3.3V)
P13			D1-	Differential Data 1-	PCIE RX-	
P14	CLK	Clock				
P15			D1+	Differential Data 1+	PCIE RX+	
P16	VSS2	Supply Voltage Ground	VSS2	Supply Voltage Ground	VSS2	Supply Voltage Ground
P17			VSS5	Supply Voltage Ground	VSS5	Supply Voltage Ground
P18	DAT0	Data Line [Bit 0]	RCLK+		REFCLK+	PCIE Ref Clock
P19					VDD3	Supply Voltage (1.2V)-TBD
P20	DAT1	Data Line [Bit 1]	RCLK-		REFCLK-	PCIE Ref Clock
P21	WP	Write Protect	WP	Write Protect	WP	Write Protect



TABLE

CHT P/N	TABLE A GOLD PLATING THICKNESS
A0912-0003	Au 30μ"
A0912-0001	G/F

PRODUCT SPECIFICATION: DOC.No: SP-XXXX-XX	SCALE 1 : 1	DESIGN UNITS mm
TEST REPORT DOC.No: TR-XXXX-XX	DRAWN Allen	DATE 2022/02/09
GENERAL TOLERANCES (UNLESS SPECIFIED)	CHECKED	DATE
X.XXX ± 0.10	APPROVED	DATE
X.XX ± 0.20		
X.X ± 0.35		
ANGULAR ± 3°	DWG NO. SA0912-001	
THIRD ANGLE PROJECTION		

CHT 群匯科技股份有限公司  
CHIUN HUEI Technology CO., LTD.

TITLE  
SD7.0 H=5.0 NON - PUSH

PART NO. SEE TABLE	DWG REV. 01	SHEET NO. 1 OF 1
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